



PATENT

Case Docket No. IMEC92.001DV1  
Date: September 3, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Brockmeyer, et al.  
Appl. No. : 10/766,159  
Filed : Janaury 27, 2004  
For : OPTIMIZED DATA  
TRANSFER AND STORAGE  
ARCHITECTURE FOR MPEG-  
4 MOTION ESTIMATION ON  
MULTI-MEDIA PROCESSORS  
Examiner : Jose L. Couso  
Group Art Unit : 2621

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

September 3, 2004

(Date)

Eric M. Nelson, Reg. No. 43,829

TRANSMITTAL LETTER

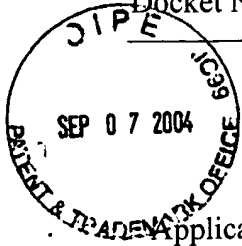
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application are:

- (X) An Information Disclosure Statement.
- (X) A PTO Form 1449 with twenty-eight (28) references.
- (X) The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Account No. 11-1410.
- (X) Return prepaid postcard.

Eric M. Nelson  
Registration No. 43,829  
Attorney of Record  
Customer No. 20,995  
(619) 235-8550



## INFORMATION DISCLOSURE STATEMENT

Applicant : Brockmeyer, et al.  
App. No. : 10/766,159  
Filed : January 27, 2004  
For : OPTIMIZED DATA TRANSFER AND  
STORAGE ARCHITECTURE FOR  
MPEG-4 MOTION ESTIMATION ON  
MULTI-MEDIA PROCESSORS  
Examiner : Couso, Jose L.  
Group Art Unit : 2621

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing 28 references that are also enclosed.

This Information Disclosure Statement is being filed before the receipt of a first Office Action on the merits, and presumably no fee is required in accordance with 37 C.F.R. § 1.97(b)(3). If a first Office Action on the merits was mailed before the mailing date of this Statement, the Commissioner is authorized to charge the fee set forth in 37 C.F.R. § 1.17(p) to Deposit Account No. 11-1410.

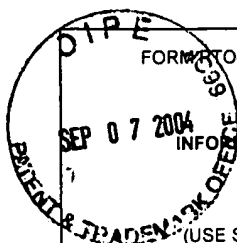
Respectfully submitted,  
KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: 9/3/2004

By: Eric M. Nelson

Eric M. Nelson  
Registration No. 43,829  
Attorney of Record  
Customer No. 20,995  
(619) 235-8550

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FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
IMEC92.001DV1APPLICATION NO.  
10/766,159INFORMATION DISCLOSURE STATEMENT  
BY APPLICANT

(USE SEVERAL SHEETS IF NECESSARY)

APPLICANT  
Brockmeyer, et al.FILING DATE  
January 27, 2004GROUP  
Unknown

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	1.	5,978,509	11/02/99	Nachtergaele et al.			

EXAMINER  
INITIAL

## OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

	2	Agarwal, R. et al., Coding of moving pictures and audio, N1693 "International organization for standardization, ISO/IEC/JTC1/SC29WG11," April 1997.
	3	Amarasinghe, S. et al., "The SUIF compiler for scalable parallel machines," in <i>Proc. of the 7th SIAM Conf. on Parallel Proc. for Scientific Computing</i> , 1995.
	4	Bacon, D.F. et al., "Compiler transformations for high-performance computing," Computer Science Division, University of California, Berkeley, California, 1994.
	5	Brodersen, R.W., "The Network Computer and its Future," <i>Proc. IEEE Int. Solid-State Circ. Conf.</i> , San Francisco CA, pp.32-36, Feb. 1997.
	6	Catthoor, F. "Energy-delay efficient data storage and transfer architectures: circuit technology versus design methodology solutions," <i>Proceedings of DATE'98</i> , Feb.23-25 1998.
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	8	Chatterjee, P. (President Personal Productivity Products, Texas Instruments), "Gigachips: deliver affordable digital multi-media for work and play via broadband network and set-top box," Plenary paper in <i>Proc. IEEE Int. Solid-State Circ. Conf.</i> , San Francisco CA, pp.26-30, Feb. 1995.
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	10	De Greef, E. et al., "Memory organization for video algorithms on programmable signal processors," <i>Proc. IEEE Int. Conf. on Computer Design</i> , Austin TX, pp.552-557, Oct. 1995.
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EXAMINER

DATE CONSIDERED

\*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (USE SEVERAL SHEETS IF NECESSARY)	ATTY. DOCKET NO. IMEC92.001DV1	APPLICATION NO. 10/766,159
	APPLICANT Brockmeyer, et al.	
	FILING DATE January 27, 2004	GROUP Unknown

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
	19 Nachtergaele, L. et al., "Optimisation of memory organisation and hierarchy for decreased size and power in video and image processing systems," <i>Proc. Intl. Workshop on Memory Technology, Design and Testing</i> , San Jose CA, pp.82-87, Aug. 1995.
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	26 van Swaaij, M. et al., "Modelling data and control flow for high-level memory management," <i>Proc. 3rd ACM/IEEE Europ. Design Automation Conf.</i> , Brussels, Belgium, pp.8-13, March 1992.
	27 Verbaauwhede, I. et al., "Background memory management for the synthesis of algebraic algorithms on multi-processor DSP chips," <i>Proc. VLSI'89, Int. Conf. on VLSI</i> , Munich, Germany, pp.209-218, Aug. 1989.
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